EVENT DETECTION WITH A DIGITAL PROCESSOR

ABSTRACT OF THE DISCLOSURE

A bistable memory device changes logic state each time an event occurs. The bistable memory device has an logic output coupled to a digital processor input. The

5   digital processor reads the logic state of the bistable memory device from its logic output and compares the logic state read to a stored previous logic state obtained from a previous read. If the logic state read and the stored previous logic state are the same, then no event has occurred during the time between the read and previous read of the logic states of the bistable memory device. If different, then an event has occurred during the

10   time between the read and previous read of the logic states of the bistable memory device. The event detection may be used in combination with a digital system communicating by serial digital data transmissions.